

## Adesto Technologies— Fusion Serial Flash

### Wide Voltage, Ultra-Low Power Products for Wearable, Mobile and Energy-Conscious Applications

Adesto Technologies introduces a new family of wide-voltage range, ultra-low power Serial Flash memory products. Targeted for wearable, mobile, and other energy conscious applications, the new "Fusion" Serial Flash family enhances Adesto's existing AT25DF products by combining industry standard sector sizes and read/write commands with new features such as wide Vcc (1.65V-4.4V), "ultra-deep power down" mode, and page erase capability. The ultra-deep power down mode allows devices to function with a class leading standby current of 200 nanoamps -- an order of magnitude improvement over standby modes available today. With these features, the new memory can extend the life of battery-operated devices such as Bluetooth low energy (BLE) products, DECT ULE (Ultra Low Energy), ZigBee RF4CE, Z-Wave and other Wi-Fi and Wi-Fi Direct applications.

#### Wide Vcc Operation:

For mobile or battery operated devices, Fusion products can run unregulated to maximize battery life from 1.65V to 4.4V uninterrupted. In a comparison of standard Vcc parts, the extended voltage range can maximize the energy utilization from the battery by as much as 1000%, significantly enhancing the battery life in the product.

#### Ultra Deep Power Down:

Fusion products offers maximum energy savings, via a simple software instruction for ultra-deep power down. The power-down mode offered is measured in nanoamps, an order of magnitude better than other competitive products. Software control of power down allows the designer to eliminate extra hardware components such as low dropout (LDO) voltage regulators, DC-DC converters or transistors, which add cost and complexity.

#### Page Erase Functionality:

With Adesto's Fusion products, designers can erase pages as small as 256 bytes when reprogramming the device. This differs from standard Serial Flash products which require a minimum of a 4Kb block erase. That means less memory management is required from the host controller, freeing it for higher priority operations. Less memory management also means a smaller software footprint (reduced code size), and lower MCU overheads. This results in much lower power requirements and gives designers the flexibility to use smaller, lower cost microcontrollers, or forego the need for supplementary external SRAM. A page erase architecture significantly enhances system capability.

The Fusion family is ideally suited for wearable, mobile, and other energy-conscious memory applications. A companion set of new devices with a narrow Vcc range (2.3-3.6V) which include the ultra-deep power down feature is also available for customers with standard voltage range requirements.

#### Key Features

- Single 1.65V - 3.6V, 2.3 - 3.6V or 1.6V - 4.4V Supply
- Serial Peripheral Interface (SPI) Compatible
- 85MHz Maximum Operating Frequency
  - Clock-to-Output (tV) of 6 ns
- Flexible, Optimized Erase Architecture for Code and Data Storage Applications
  - Uniform 256-Byte Page Erase
  - Uniform 4-Kbyte Block Erase
  - Uniform 32-Kbyte Block Erase
  - Full Chip Erase
- Hardware Controlled Locking of Protected Sectors
- Fast Program and Erase times
  - 1.5ms Typical Page Program (256 Bytes) Time
  - 50ms Typical 4-Kbyte Block Erase Time
  - 400ms Typical 32-Kbyte Block Erase Time
- Automatic Checking and Reporting of Erase and Program Failures
- Software Controlled Reset
- JEDEC Standard Manufacturer and Device ID Read
- Low Power Dissipation
  - 200nA Ultra Deep Power Down (Typical)
  - 5µA Deep Power-Down (Typical)
  - 25uA Standby current (Typical)
  - 5mA Active Read Current (Typical)



# Adesto® Serial Flash/Fusion Memory Products Selector

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