AC/DC Secondary-Side Controller for Qualcomm® Quick Charge™ 2.0 USB Interface and Secondary-Primary Communication

1 Description

The iW626 is an AC/DC secondary-side controller for QC2.0 USB interface and secondary-primary communication. The device allows rapid charging of QC2.0-enabled PDs. The iW626 resides on the secondary side of an AC/DC power supply and allows the adapter to be configured for 5V, 9V, or 12V output depending on the voltage requested by QC2.0 enabled PD. It can be used in Dialog's primary-side controlled AC/DC systems to achieve fast voltage transition, low no-load power consumption and fast dynamic load response. The iW626 implements Dialog's proprietary secondary-to-primary digital communication technique. When paired with Dialog's primary-side controller iW1780, the iW626 eliminates the discrete decoders in the primary side and simplifies system designs. The iW626 uses one opto-coupler to transmit all necessary information for rapid charge, including output voltage requests, output current limits, output voltage undershoot and output over-voltage. It also has a built-in opto-coupler LED driver to minimize the bill of material cost.

Dialog's innovative proprietary technology ensures that power supplies designed with the iW626 and iW1780 can provide 5V/9V/12V output voltage configuration, with user-selected various output current limit combinations. Furthermore, the chipset can achieve <10mW no-load power consumption at 5V2A output setting and fast dynamic load response in typical AC/DC HVDCP adapter designs.

Qualcomm® Quick Charge™ 2.0 is a product of Qualcomm Technologies, Inc.

2 Features

- Supports Qualcomm® Quick Charge™ 2.0 (QC2.0) technology High Voltage Dedicated Charging Port (HVDCP) Class A with voltage configuration of 5V, 9V, and 12V
- Proprietary secondary-to-primary digital communication eliminates discrete decoders in the primary side and simplifies system designs
- Single opto-coupler transmits all information for rapid charge: output voltage requests, output current limits, output voltage undershoot, and over-voltage protection
- Backward compatible with USB Battery Charging Specification Revision 1.2 (USB BC1.2) Dedicated Charging Port (DCP)
- Programmable active fast discharge from 9V/12V to 5V at portable device (PD) unplug or from a high voltage level to a lower level upon request with built-in switch or external switch
- Normally OFF state with <120µA cut-off current during 5V steady-state operation to achieve <10mW power consumption at no load
- Wide operating voltage range from 3V to 25V
- Built-in opto-coupler LED driver with minimum driving current of 2mA
- 6-lead SOT-23 package

3 Applications

- Rapid-charging AC/DC adapters for smart phones, tablets, and other portable devices

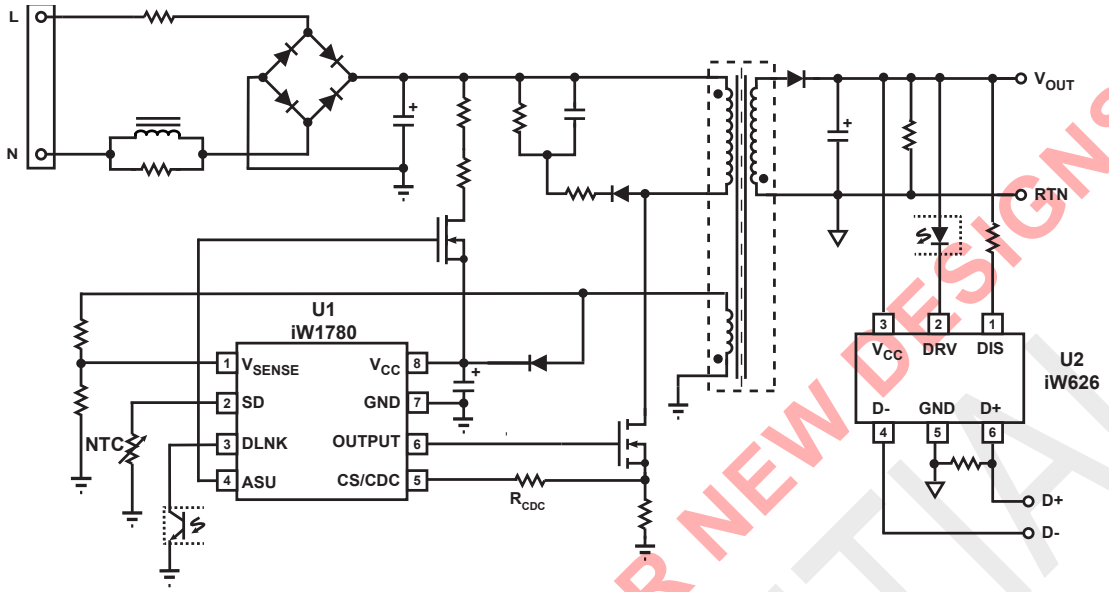


Figure 3.1 : iW626 Typical Application Circuit for Multi-Level Output Voltage and Current (Using iW1780 as Primary-Side Controller. Achieving <10mW No-Load Power Consumption)

4 Pinout Description

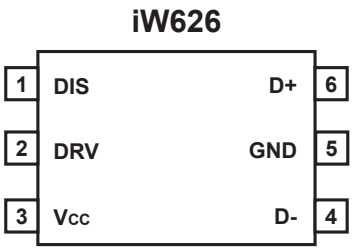


Figure 4.1 : 6-Pin SOT23 Package

Pin No.	Pin Name	Type	Pin Description
1	DIS	Analog Output	Discharging circuit and external FET drive. Used for fast discharging of output capacitor. Also can be used for driving external FET.
2	DRV	Analog Output	External circuit drive. Can be used for opto-coupler LED drive with automatic current limiting for transmitting signals to primary side.
3	V_{CC}	Power Supply	IC power supply.
4	D-	Analog Input	USB D- signal.
5	GND	Ground	Ground.
6	D+	Analog Input	USB D+ signal.

5 Absolute Maximum Ratings

Absolute maximum ratings are the parameter values or ranges which can cause permanent damage if exceeded. For maximum safe operating conditions, refer to Electrical Characteristics in Section 6.

Parameter	Symbol	Value	Units
DIS (pin 1) voltage	V_{DIS}	30	V
DRV (pin 2) voltage	V_{DRV}	30	V
Vcc (pin 3) voltage	V_{CC}	30	V
D- (pin 4) voltage	V_{D-}	-0.3 to 7	V
D+ (pin 6) voltage	V_{D+}	-0.3 to 7	V
Continuous DC current at DRV pin (V_{DRV}= 11V)	I_{DRV}	25	mA
Continuous DC supply current at V_{CC} pin (V_{CC}= 12V)	I_{VCC}	25	mA
Peak current at DIS pin (V_{DIS}= 12V)	I_{DIS}	600	mA
Maximum junction temperature	T_{JMAX}	150	°C
ESD rating per JEDEC JESD22-A114 (D+ and D- pins)		4,000	V
ESD rating per JEDEC JESD22-A114 (all other pins)		2,000	V

6 Electrical Characteristics

V_{CC} = 12V, -40°C ≤ T_A ≤ 85°C, unless otherwise specified

Parameter	Symbol	Test Conditions	Min	Typ	Max	Unit
V_{CC} SECTION (Pin 3)						
Standby operating current (Note 1)	I_{CC}	No dynamic preload, V_{CC} = 5V			120	µA
Operating voltage (Note 1)	V_{CC}				25	V
Start-up threshold (Note 1)	$V_{CC(ST)}$			3.3		V
Undervoltage lockout threshold (Note 1)	$V_{CC(UVL)}$			3.0		V
Active fast discharge threshold (Note 1)	$V_{CC(DIS)}$		+7.5	+10	+12.5	%
Under-voltage threshold (iW626-00)	$V_{CC(UV)}$			-4		%
Under-voltage threshold (iW626-02/04)				-6		%
Over-voltage threshold	$V_{CC(OV)}$			+25		%
DRV SECTION (Pin 2)						
DRV pin sink current	I_{DRV}		2		10	mA
DIS SECTION (Pin 1)						
DIS pin sink current (Note 1)	I_{DIS}	ON state			500	mA
D+ and D- SECTION (Pin 6 and Pin 4)						
Data detect voltage	V_{DAT_REF}		0.25	0.325	0.4	V
V_{OUT} selection reference	V_{SEL_REF}		1.8	2	2.2	V
D+ to D- resistance	R_{DCP_DAT}	D+=D-=0.6V			40	Ω
D- pull-down resistance	R_{DM_DWN}		14.25	19.53	24.80	kΩ
TIMING SECTION (Pin 4)						
Voltage change glitch filter (Note 1)	$T_{GLITCH\_V\_CHANGE}$		20	40	60	ms
New voltage request interval (Note 1)	$T_{V\_NEW\_REQUEST}$		200			ms

Notes:

Note 1. These parameters are not 100% tested. They are guaranteed by design and characterization.

7 Typical Performance Characteristics

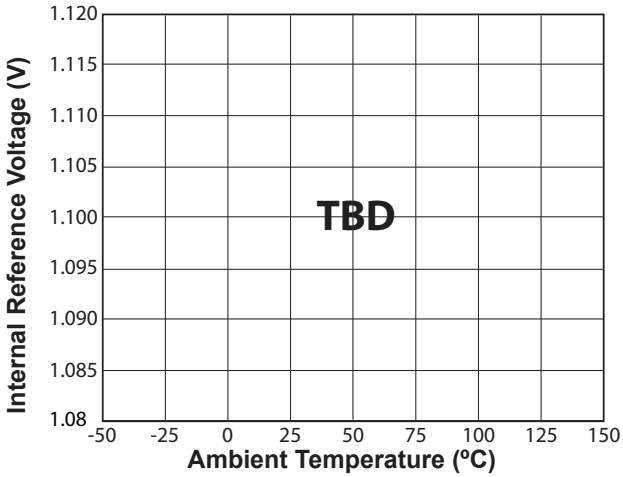


Figure 7.1 : Internal Reference Voltage vs. Ambient Temperature

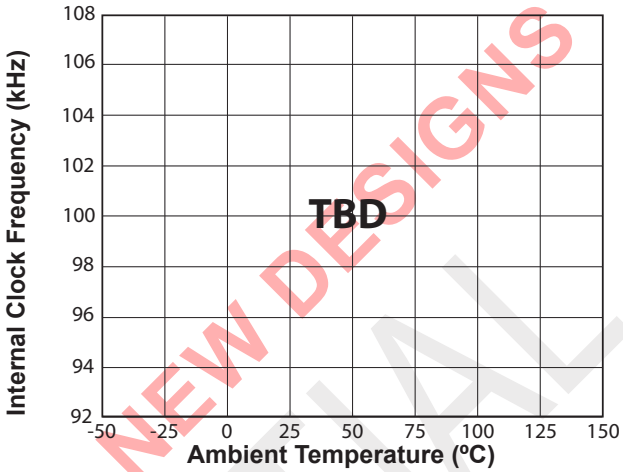


Figure 7.2 : Internal Clock Frequency vs. Ambient Temperature

8 Functional Block Diagram

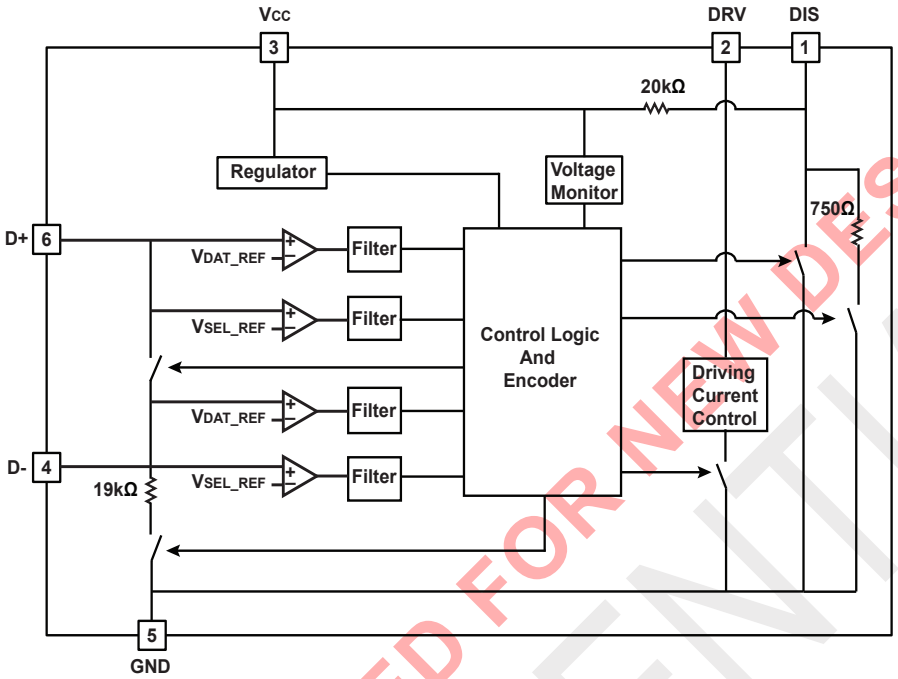


Figure 8.1 : iW626 Functional Block Diagram

9 Theory of Operation

The iW626 is an AC/DC secondary-side controller for QC 2.0 USB interface and secondary-primary communication interfacing with PDs equipped with QC2.0 technology to configure the adapter output voltage to 5V/9V/12V levels for rapid charging. The iW626 is also backward compatible with USB BC1.2 compliant PDs and other PDs to provide 5V output voltage by default setting. The iW626 can be detected as a DCP and further identified as an HVDCP if a QC2.0 equipped PD is connected. After the initial detection stage, the iW626 interprets D+/D- signal combinations to 5V/9V/12V output voltage requests. A valid request is encoded to certain pulse patterns and sent to the primary side through an opto-coupler. The constant current (CC) limit setting is also sent together with the voltage request. Besides the voltage and current information, the iW626 also monitors the adapter output voltage and sends over-voltage or under-voltage information to primary side through the same opto-coupler with Dialog's proprietary secondary side to primary side digital communication protocol. An internal circuit is designed for limiting the driving current to typical operating forward current range of the opto-coupler LED so that the driving current is independent of output voltage. The iW626 should be paired with Dialog's primary-side controller, iW1780, for dynamic QC2.0 voltage and current configuration and control. The iW1780 has a built-in circuit to decode the different pulse patterns for voltage configuration, current limit setting, V_{OUT} under-voltage and over-voltage detection, and based on the decoded information, the iW1780 responses accordingly. The iW626 also features a programmable fast/slow active discharging function to discharge the output capacitor in a short time after a request for a lower voltage or unplug of the QC2.0 equipped PDs.

9.1 Pin Detail

Pin 1 – DIS

Programmable active discharge. This pin provides fast and slow discharge paths for the external circuit, such as an output capacitor. It can also drive an external P-channel FET. When there is a request for a lower voltage or the USB PD is unplugged at a high voltage, the internal active discharge switches are turned on.

Pin 2 – DRV

External circuit drive. This pin drives the external circuit, such as the opto-coupler, to send out all the information for rapid charge. The DRV pin sink current at ON state is limited to a range such that a low cost opto-coupler can be used.

Pin 3 – V_{CC}

IC power supply. This pin provides the IC supply voltage.

Pin 4 – D-

USB D- signal.

Pin 5 – GND

Ground.

Pin 6 – D+

USB D+ signal.

9.2 Initialization and Handshaking

An AC/DC power adaptor designed with the iW1780 and iW626 starts up initially at default 5V output voltage. When the output voltage of the AC/DC adapter, which is also the V_{CC} voltage for the iW626, rises above $V_{CC(ST)}$, the iW626 begins to work at 5V state and the D+/D- short switch is turned on. The USB BC1.2 or QC2.0 compliant PD detects the D- voltage while applying a voltage on D+ and vice versa. The iW626 is designed such that the impedance between D+ and D- is low enough to meet the specifications of USB BC1.2 and QC2.0 during the initial detection when the D+/D- short switch is on. The iW626 ensures that D+ stays between V_{DAT_REF} and V_{SEL_REF} for at least

1.25 second without any glitch before it turns off the D+/D- short switch and turns on the D- pull-down switch. The handshaking between the iW626 and QC2.0-equipped PD finishes and the iW626 starts to take voltage requests from the PD after D- is pulled down for 20ms.

9.3 Voltage and Current Request Interpretation and Encoding

The iW626 monitors the voltage changes of D+ and D-. If there is a D+/D- combination change and the last output voltage transition period is over, the iW626 uses a 40ms de-glitch filter timer to validate the D+/D- signals and interprets the D+/D- combination according to QC2.0 specification. The interpretation of D+/D- combination and the voltage requests are listed in Table 9.1.

Please note that a voltage at D+ or D- is detected as:

a) 0V, if it is lower than V_{DAT_REF};

b) 0.6V, if it is between V_{DAT_REF} and V_{SEL_REF};

c) 3.3V, if it is higher than V_{SEL_REF}.

D+	D-	V_{OUT}
0.6V	0.6V	12V
3.3V	0.6V	9V
0.6V	0V	5V
0V	0/0.6/3.3V	5V
All other combinations		Stays unchanged

Table 9.1: D+/D- Signals and Adapter V_{OUT} (aka V_{BUS}, V_{CC})

The iW1780 uses the patented primary-feedback control to achieve the multi-level constant-voltage and multi-level constant-current (CC) regulations. The CC limit is given by

$$I_{CC\_LIMIT} = \frac{k}{2} \times \frac{N}{R_S} \times \eta_x$$

where N is the transformer primary to secondary side winding turns ratio, R_S is the current sense resistor, η_X is the transformer conversion efficiency, and k is a coefficient set by the iW626 (see Section 11 for pre-defined k information).

The iW626 has a built-in encoder to generate different pulse patterns and drive the internal switch of DRV pin so that the different voltage information together with the associated current limit setting can be sent to the primary side through an opto-coupler.

9.4 Programmable Active Discharge

Discharge of output capacitor is necessary for quick voltage transition from a higher level to a lower level when there is a lower voltage request. It is even more important to discharge the output capacitor quickly from a high level to 5V after the PD is unplugged in order to ensure the safety of other non-QC2.0 compliant PDs. An internal switch between the DIS pin and GND pin is turned on to provide a path from the output voltage through an external resistor, the DIS pin, and the internal switch to ground. The discharging time is programmable with the external resistor. The resistance of 47Ω or higher is recommended for the external resistor to prevent over-current or over-heat inside the IC. If certain application uses a larger output capacitor or requires faster discharging, an external P-channel FET can be used and the iW626 DIS pin can be used to drive the FET. When the PD is unplugged, the iW626 resets to its initial setting. The active fast discharge starts after 40ms de-glitch of a valid D+/D- combination or after 10ms de-glitch of PD unplug; it stops when the active discharge threshold of target voltage is reached or a 200ms timer (including de-glitch time) expires to avoid excess load current and high power dissipation inside the IC. After the active fast discharge stops, a

slow discharge path continues to discharge the output capacitor until the 200ms timer expires. For effective PD unplug detection, a resistor between 300kΩ and 1.5MΩ should be implemented between D+ and GND.

9.5 Dynamic Preload

Flyback converter with primary-side sensing usually has a preload resistor in order to balance the power at no-load condition. If the preload resistor is sufficient for power balance at 5V output, it consumes the excessive power at 9V or 12V standby mode. An 11kΩ internal dynamic preload resistor is implemented in the iW626 between V_{CC} and GND at 5V state only. On the other hand, a fixed external preload resistor can be used in addition to the internal dynamic preload resistor for fine-tuning the preload at 5V state, and for the preload at 9V and 12V states for low standby power consumption, while the internal dynamic preload resistor is disconnected.

9.6 V_{BUS} Monitoring

In addition to encoding and transmitting the output voltage and current request, the iW626 monitors the V_{BUS} for both under-voltage and over-voltage. The V_{BUS} under-voltage or voltage undershoot is usually caused by a sudden load current increase. The iW626 also monitors the V_{BUS} over-voltage, especially the over-voltage caused by the output voltage setting mismatch between the iW1780 and iW626.

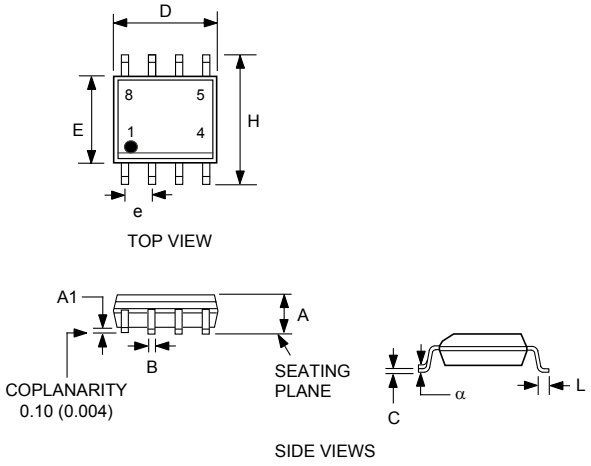
When a load transient event from light load to heavy load happens, the output voltage drops. If the output voltage drops to the voltage undershoot threshold, the iW626 turns on the LED of the opto-coupler by controlling the DRV pin sink current, and the DLNK pin of the iW1780 is pulled down by the transistor of the opto-coupler. After the iW1780 receives this DLNK pin signal, it can intelligently confirm if this signal is caused by an undershoot event and distinguish it from a voltage and current request, and then it promptly increases the switching frequency and the t_{ON} to delivery more power to the secondary side in order to bring the output voltage back to regulation. The undershoot detection signal of the iW626 is backward compatible with Dialog's secondary-side voltage position monitor, the iW628.

While the iW1780 can protect against the V_{BUS} over-voltage through the V_{SENSE} signal in most condtions, it is difficult for the iW1780 to protect the over-voltage caused by output voltage setting mismatch between the iW1780 and the iW626. If V_{SENSE} signal scaling is not set correctly by the iW1780 digital logic due to the wrong state caused by the mismatch, the over-voltage can not be detected through V_{SENSE} at the primary side. The iW1780/iW626 chipset added one more layer of OVP. When the V_{BUS} rises to above the over-voltage threshold of the iW626's present setting, the iW626 drives the DRV pin in a special switching pattern serving as a OVP signal and turns on both the fast and slow discharge. After the iW1780 receives this OVP signal, it shuts down the power supply promptly.

In this way, through the single opto-coupler and proprietary digital communication the iW626 transmits to the iW1780 all the necessary information for a high-performance rapid-charge AC/DC system design including output voltage requests, output current limits, output voltage undershoot and output over-voltage.

10 Physical Dimensions

8-Lead Small Outline (SOIC) Package



Symbol	Inches		Millimeters	
	MIN	MAX	MIN	MAX
A	0.053	0.069	1.35	1.75
A1	0.0040	0.010	0.10	0.25
B	0.014	0.019	0.35	0.49
C	0.007	0.010	0.19	0.25
D	0.189	0.197	4.80	5.00
E	0.150	0.157	3.80	4.00
e	0.050 BSC		1.27 BSC	
H	0.228	0.244	5.80	6.20
L	0.016	0.049	0.40	1.25
α	0°	8°		

Compliant to JEDEC Standard MS12F

Controlling dimensions are in inches; millimeter dimensions are for reference only

This product is RoHS compliant and Halide free.

Soldering Temperature Resistance:
[a] Package is IPC/JEDEC Std 020D moisture sensitivity level 1
[b] Package exceeds JEDEC Std No. 22-A111 for solder immersion resistance; package can withstand 10 s immersion < 260˚C

Dimension D does not include mold flash, protrusions or gate burrs. Mold flash, protrusions or gate burrs shall not exceed 0.15 mm per end. Dimension E1 does not include interlead flash or protrusion. Interlead flash or protrusion shall not exceed 0.25 mm per side.

The package top may be smaller than the package bottom. Dimensions D and E1 are determined at the outermost extremes of the plastic body exclusive of mold flash, tie bar burrs, gate burrs and interlead flash, but including any mismatch between the top and bottom of the plastic body.

11 Ordering Information

Part No.	Options	Package	Description
iW626-00	k=0.5/0.411/0.322 for CC limits for 5/9/12V configuration, 4.8V undershoot threshold for 5V, 11kΩ dynamic preload at 5V	SOT-23	Tape & Reel[1]
iW626-02	k=0.411 for CC limits for 5/9/12V configuration, 4.7V undershoot threshold for 5V, 11kΩ dynamic preload at 5V	SOT-23	Tape & Reel[1]
iW626-04	k=0.411 for CC limits for 5/9V configuration, k=0.322 for 12V, 4.7V undershoot threshold for 5V, 11kΩ dynamic preload at 5V	SOT-23	Tape & Reel[1]
iW626-06	k=0.5/0.322/0.231 for CC limits for 5/9/12V configuration, 4.7V undershoot threshold for 5V, 11kΩ dynamic preload at 5V	SOT-23	Tape & Reel[1]
iW626-20	k=0.5/0.411 for CC limits for 5V/9V configuration, 12V is disabled with V_{OUT} remaining unchanged upon 12V request, 4.8V undershoot threshold for 5V, 11kΩ dynamic preload at 5V. See Note 2.	SOT-23	Tape & Reel[1]
iW626-21	k=0.5/0.411 for CC limits for 5V/9V configuration, 12V is disabled with V_{OUT} remaining unchanged upon 12V request, 4.8V undershoot threshold for 5V, no dynamic preload. See Note 2.	SOT-23	Tape & Reel[1]
iW626-24	k=0.411 for CC limits for 5V/9V configuration, 12V is disabled with V_{OUT} remaining unchanged upon 12V request, 4.8V undershoot threshold for 5V, 11kΩ dynamic preload at 5V. See Note 2.	SOT-23	Tape & Reel[1]

Note 1: Tape and reel packing quantity is 3,000/reel. Minimum ordering quantity is 3,000.

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Contacting Dialog Semiconductor

United Kingdom (Headquarters)
Dialog Semiconductor (UK) LTD
Phone: +44 1793 757700

Germany
Dialog Semiconductor GmbH
Phone: +49 7021 805-0

The Netherlands
Dialog Semiconductor B.V.
Phone: +31 73 640 8822

Email
info_pcbg@diasemi.com

North America
Dialog Semiconductor Inc.
Phone: +1 408 845 8500

Japan
Dialog Semiconductor K. K.
Phone: +81 3 5425 4567

Taiwan
Dialog Semiconductor Taiwan
Phone: +886 281 786 222

Web site:
www.dialog-semiconductor.com

Singapore
Dialog Semiconductor Singapore
Phone: +65 64 8499 29

Hong Kong
Dialog Semiconductor Hong Kong
Phone: +852 3769 5200

Korea
Dialog Semiconductor Korea
Phone: +82 2 3469 8200

China (Shenzhen)
Dialog Semiconductor China
Phone: +86 755 2981 3669

China (Shanghai)
Dialog Semiconductor China
Phone: +86 21 5424 9058